



*FEE only*

*IFW*

501.43323X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: A. NISHIDA et al  
Serial No.: 10/781,861  
Filed: February 20, 2004  
For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND A METHOD OF MANUFACTURING THE SAME  
Group: 2815  
Examiner: M. WARREN

**AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT**

**MS: AMENDMENTS**

Commissioner for Patents  
POB 1450  
Alexandria, VA 22313-1450

July 18, 2005  
(Monday)

Sir:

In response to the Restriction Requirement, dated June 16, 2005, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims; and  
Remarks are included following the Amendments.**

08/02/2005 LWSE1 00000001 012135 10781861

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